Application Number:	106	10672961						
Filing Date:	26-	26-Sep-2003						
Title of Invention:		Three dimensional multi layer memory and control logic integrated circuit structure						
First Named Inventor/Applicant Name:	Gle	Glenn J. Leedy						
Filer:	Mic	Michael James Ure						
Attorney Docket Number:	ELA	ELM-2 CONT. 4						
Filed as Small Entity								
Utility under 35 USC 111(a) Filing Fees								
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)			
Basic Filing:								
Pages:					0			
Claims:								
Claims in excess of 20		2202	6	26	156			
Miscellaneous-Filing:								
Petition:								
Patent-Appeals-and-Interference:								
Post-Allowance-and-Post-Issuance:								

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
	Total in USD (\$)			156